

Información del Plan Docente

Academic Year	2017/18
Faculty / School	110 - Escuela de Ingeniería y Arquitectura
Degree	439 - Bachelor's Degree in Informatics Engineering
ECTS	6.0
Year	4
Semester	Indeterminate
Subject Type	Compulsory
Module	---

1.General information**1.1.Introduction****1.2.Recommendations to take this course****1.3.Context and importance of this course in the degree****1.4.Activities and key dates****2.Learning goals****2.1.Learning goals****2.2.Importance of learning goals****3.Aims of the course and competences****3.1.Aims of the course****3.2.Competences****4.Assessment (1st and 2nd call)****4.1.Assessment tasks (description of tasks, marking system and assessment criteria)****5.Methodology, learning tasks, syllabus and resources****5.1.Methodological overview**

This class enables students to acquire the set of skills and abilities required to deal with semester-long projects. Monthly, students have to complete milestones and present their progress in the lab. Therefore, there are almost no theory classes. Milestones enable a continuous tracking of the students, and, at each milestone, students have to defend their progress with real hardware demos and technical written reports showcasing their designs.

30241 - Embedded Systems Laboratory

5.2.Learning tasks

The schedule includes the following activities:

1. Lectures: In these classes an introduction to each project will be done by reviewing the necessary theoretical knowledge, relating the knowledge acquired in previous courses, describing the support materials available, and briefly explaining the tasks.
2. Laboratory sessions (3 hours per week throughout the course), working in the laboratory with a teacher.
3. Study and personal work (55 estimated hours): students work on their own, using the material available to acquire the necessary skills and develop the requested project.
4. Drafting of documentation (20 hours): After completing the project, students must submit a report.
5. Deliveries and corrections (5 hours): Students must periodically submit the work to one of the teachers of the subject. These deliveries serve both to evaluate the student and to guide him. In addition teachers will review the reports submitted by students and provide feedback.

5.3.Syllabus

Project development of an embedded system, hardware and software with real-time constraints. Performance analysis. Students have to defend their progress with real hardware demos and technical written reports showcasing their designs.

5.4.Course planning and calendar

Class sessions are held in the laboratory according to schedule set by the center (schedules available on their website).

Each teacher will inform its hours of tutoring.

The other activities will be planned depending on the number of students and will be announced in advance. It can be found at the course web site at <http://moodle.unizar.es>

5.5.Bibliography and recommended resources

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